

TECHNICAL -VISIO STENCIL ENGINEERING

NOTIX - VE [19 TH OCTOBER 2021]

WWW.TCVECTORS.COM [TCVECTORS-WINELECTRRIX]
BLUE PRINTS [NOTIX]

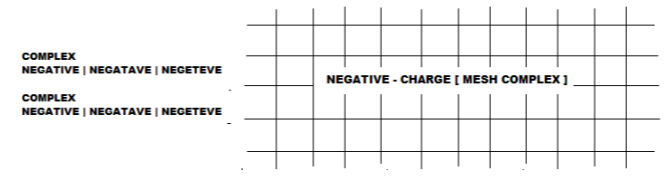
MACHINE PLAN - [SYSTEMS ENGINEERING - INTELLIGENT WIDE FRAMEWORK APPROACH]

COMPUTE MACHINERY - DEVELOPMENT [A14 - T4] STRATEGY

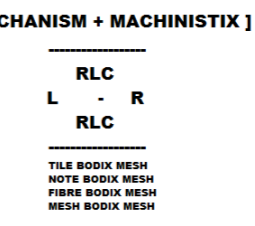
A1	A2	A3	A4	A5	A6	A7
ELECTRONIC FABRICATION [MANUFACTURING]	NETWORKS COMMUNICATION RADIO - TOWERS CDMA PROTOCOLS GSM - DSL - TCP - IP - UDP - HTTP	SERVERSIDE ENGINEERING [.NET] [JAVA] INTERFACE DESIGN - HTML INTERACTIVES FILE SYSTEM - READ WRITE DATA - STRUCTURE LINKED LIST	OPERATING SYSTEMS INTERNALS FILE SYSTEMS WINDOWS AIX - UNIX	CPU - MOTHERBOARD GRAPHICS - SUBSYSTEMS MEMORY CHIPS COMPUTER MONITOR SOUND CARDS LAN CARDS MODEMS	COMPUTER ARCHITECTURE & ORGANIZATION HARDWARE INTERNALS [HW - SW BINARY] COMPLETE COMPUTER [SYSTEMS PICTURE]	ELECTRONIC CIRCUITS PRINTED CIRCUIT BOARDS R-L-C CIRCUITS NORTH BRIDGE SOUTH BRIDGE EMBEDDED - SYSTEMS EMBEDDED ELECTRONICS
A7	A9	A10	A11	A12	A13	A14
COMPUTER - APPLICATIONS GAMES AND TOOLS WIKI - STORE YOUTUBE GOOGLE BENCHMARKING TROUBLESHOOTING	COMPUTER - STORAGE DATABASES - DATA - STORE DATA DESIGN - DATA ARCHITECTURE	COMPILERS - UTF8 - OPCODE F# - DEVELOPMENT STUDIO VISUAL STUDIO ASSEMBLERS ASSEMBLY LANGUAGE FCX- REALITY COMPILERS	INTERFACES EXPERIENCES CATHODE RAY TUBE - MONITOR LED - MONITORS LCD - DISPLAY PROJECTORS - LIGHT PROJECTORS - LED ZEISS - VISOR - EXPERIENCE	TRANSISTORS INDUCTORS ELECTRICAL COMPONENTS SEMICONDUCTORS TIMERS [MATERIALS SCIENTIFIC] MULTI MATERIAL [SILICON - GERMANIUM] [CARBON - GRAPHITE]	SIMULATORS TRAINERS EXPERIENCE SYSTEMS NEURO INTERACTIVE SYSTEMS VIRTUAL REALITY	SERVER CLUSTERS CLIENT SERVER - LAN WAN GRID ENGINEERING STORAGE AREA NETWORKS SAN NAS HYPERVISORS

- T1** DIVERSIFIED - INDUSTRIAL [SPECIALIZED SOFTWARE AND SYSTEMS SOFTWARE]
[HARDWARE + SOFTWARE] - INTEGRATED PACKAGED SYSTEMS [WIDE EXPERIENCE]
LEVEL 2 - PLC - ROBOTICS - AUTOMATION [INDUSTRIAL - FACTORIES AND MANUFACTURING]
- T2** CONTROL SYSTEMS - SCADA - ENTERPRISE FLUX - FOCUS - MARK MONITOR BASIC
- T3** ARCFINITIVE | REFINITIVE [H/W + S/W - INTEGRATED]
HARDWARE + SOFTWARE - INTEGRATED TO ONE
- T4** VISIO STENCIL - [ENGINEERING | EFINEERING]
AXISS /RR - LED DRIVE [EMULEX]
INTELLIGENT DESIGN INDUSTRIAL LOGIC - FACTORIZING ENGINE [FACTOR E - CHAOS GALACTIC SPIN - FACTORIZING]
ADVANCED HARDWARE ENGINEERING [MECHIX] - PHILOSOPHY [ESCALE - EF SCALE - EFFORT SCALE MAINFRAME]

STAT FRAME DEVELOPMENT [STAT FRAME MESH]



MECHANISTIX [MECHANISM + MACHINISTIX]



A9T4F2 - VISIO STENCIL [MAIN FRAME]

MESH DEVELOPMENT

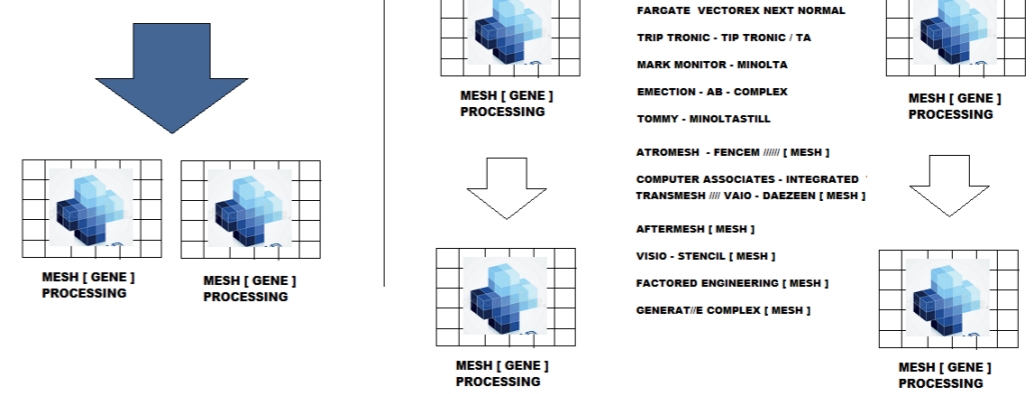
[VERGE ENGINEERING - EFINEERING]

MATROX / ADAPTIVE DRIVES - S3 VIRGE - ATI RADEON - NVEDEA [DRIVES -INTEGRATED TO TEKTO MATIC - AVEC LUMIC - AVEC MATIC]

ANTE ARCTIV VERGE
AXISS / RR DRIVE CPU - MECHANISTIX [INTELLIGENT CPU] [ACTIVE MESH CPU]
TREF/FEC - MACHINE PLANS - TRON WORLDS
IMMATION [FULLY INTEGRATED WEAVES AND SYSTEM STORE FLOOR STORE]
AEFFE / DEFFE

VISIO - STENCIL [WIDE FRAME - DEVELOPMENT]

VISIO - STENCIL - [WIDE FRAME - MAIN FRAME] DEVELOPMENT



UNDERSTAND [INTERPRETATION] - LEARN AND UNDS FROM EXAMPLES
THINK

EMECTION AB-ENVECT TOMMY-MINOLTASTILL ATROMESH-FENCEM

FARGATE-ANSTAD-ENSTED-NNSTED
EMECTIONAB-ENVECT
ATROMESH-FENCEM
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EMECTION - AB - COMPLEX [STUDIES] [INTERPRETATIONS] [REVELATIONS]

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